Modified Ramp-Reset Waveform Robust for Variable Panel Temperature and its Discharge Characteristics

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Abstract

By the voltage threshold (Vt) close-curve measurement method, the changes in the discharge characteristics such as a firing voltage and IR emission among the three electrodes were examined relative to the low or high panel temperature ranging from –10 to 80°C. The variation in the panel temperature was found significantly influence the surface discharge between the MgO surfaces rather than the plate gap discharge between the MgO and phosphor layers. Based on this experimental observation, a modified reset waveform that alleviates the surface discharge during a ramp-up and -down period was developed. By adopting the proposed reset waveform, a stable address discharge could be obtained irrespective of the panel temperature variation.

Keywords: panel temperature, Vt close-curve, reset waveform

1. Introduction

Recently, the image quality of plasma display panels is enhancing rapidly and considerably [1]. However, in order to further improve the image quality of AC-PDPs, a full high definition (HD) PDP requires stable driving condition with a wider driving margin irrespective of the variable panel temperature. The panel temperature or ambient temperature is one of the important factors determining the stable discharge in PDP cells because the discharge characteristics are varied by the panel temperature. Despite this, there has only been a few researches on done the relation between the panel or ambient temperature and the discharge characteristics [2-4].

In this paper, the changes in the discharge characteristics such as a firing voltage and IR emission among the three electrodes (X-Y, A-Y, and Y-A) were examined relative to the low or high panel temperature ranging from –10 to 80°C. In this study, the investigation was based on the Vt close-curve analysis[5, 6]. Using the experimental observations, a modified reset waveform that can produce a stable address discharge irrespective of the panel temperature variation is proposed.

2. Observation on Discharge Characteristics under Variable Panel Temperature

Fig. 1 shows the schematic diagram of the experimental setup for measurement. The 4-in. test panel with a gas mixture of Ne + Xe (4 %) and a pressure of 400 Torr was employed in this research. Its structure and dimensions were the same as those of the conventional 42-in. wide VGA grade PDP with a cell pitch of 360 μm. The panel temperature was varied from –10 to 80°C by heating the glass of the front panel with an external heater. In order to observe the discharge characteristics under variable panel temperature, the Vt close-curve was measured, as shown in Fig. 2. Fig. 2 shows the Vt close-curves measured from the test panel when the panel temperatures were –10, +25, and +80°C, respectively. As shown in the arrows of a and b
in the Vt closes-curve of Fig. 2, the measured Vt close-curve shows that the surface discharge between the X and Y electrodes (case ⓐ) were influenced more than the plate gap discharge between the A and Y electrodes under the variable panel temperature. More detailed data are given in Table 1. From Table 1, the firing voltage of the X-Y discharge can be seen to have increased at a low panel temperature (ΔV = +10 V), whereas it decreased at a high panel temperature (ΔV = -8 V). However, the firing voltages of the A-Y and Y-A discharges are little changed at

Table 1 | Changes in discharge starting voltage among three electrodes relative to variable panel temperature.

<table>
<thead>
<tr>
<th></th>
<th>X-Y discharge</th>
<th>A-Y discharge</th>
<th>Y-A discharge</th>
</tr>
</thead>
<tbody>
<tr>
<td>MgO cathode</td>
<td>Surface discharge</td>
<td>Plate gap discharge</td>
<td></td>
</tr>
<tr>
<td>-10°C</td>
<td>230 V</td>
<td>172 V</td>
<td>220 V</td>
</tr>
<tr>
<td>25°C</td>
<td>220 V</td>
<td>172 V</td>
<td>218 V</td>
</tr>
<tr>
<td>80°C</td>
<td>212 V</td>
<td>168 V</td>
<td>216 V</td>
</tr>
</tbody>
</table>

Fig. 3. Light waveforms emitted from discharge cells at various panel temperatures: (a) X-Y discharge, and (b) A-Y discharge.
the low ($\Delta V = 0 \sim 2$ V) and high panel temperature ($\Delta V = -2 \sim 4$ V) conditions.

Figs. 3 (a) and (b) illustrate the test pulses for measuring separately the IR emission from (a) the surface (X-Y) and (b) plate gap (A-Y) discharges with a variation in the panel temperature, respectively. The initial wall charges were completely removed by the erase pulse, and the application time of the detecting ramp pulse was delayed enough to remove the priming particles. After that, the detecting ramp pulses were applied to produce only the X-Y discharge as shown in Fig. 3 (a) and only the A-Y discharge in Fig. 3 (b). In this detecting pulse, the slopes of the detecting ramp pulses for measuring only the XY discharge were 4 V/µs for the X electrode, 2 V/µs for the A electrode, respectively, and the slopes of the detecting ramp pulses for measuring only the AY discharge were 2 V/µs for the X electrode, 4 V/µs for the A electrode, respectively.

The resultant IR emissions were measured for both cases. For the X-Y discharge, the discharge in the high panel temperature was initiated more quickly than that in the room panel temperature, whereas the discharge was initiated slower for the low panel temperature. However, the discharge intensities for both the high and low panel temperatures were observed to be weaker than that in the room panel temperature. For the A-Y discharge, the discharge initiation times are almost the same for both cases, but the discharge intensities were weaker for the low and high panel temperatures.

3. Modified Ramp-Reset Waveform Robust for Variable Panel Temperature

As observed in the previous section, the variable panel temperature can affect the X-Y surface discharge more than the A-Y plate gap discharge. Accordingly, the reset waveform stable for the variable temperature requires that the contribution of the surface (X-Y) discharge sensitive to the panel temperature in the reset discharge. In this sense, the conventional reset waveform that uses the XY discharge dominantly needs to be modified to reduce the effect of the temperature.

Fig. 4 (a) shows the conventional reset waveform and Fig. 4 (b) shows the proposed reset waveform with a ramp-type X bias for minimizing the variation in the reset and address discharge characteristics with the panel temperature. The basic idea of the proposed reset waveform is to maxi-

![Fig. 4. Schematic diagram of (a) conventional reset waveform and (b) proposed reset waveform with ramp-type X bias.](image_url)

![Fig. 5. Analysis of reset waveform using Vt close-curve.](image_url)
mize the plate gap (A-Y) discharge by reducing the panel temperature in the reset discharge during the ramp-up and -down periods. From Fig. 5 (a) which shows the Vt close-curve that was measured at point ① by applying the convention ramp waveform was applied as shown in Fig. 4 (a), the Y-X discharge is produced as the voltage applied to the Y electrode increases, and many weak Y-X discharges are not produced until the discharge point reaches the simultaneous discharge point. In this case, the weak reset discharge tends to be very sensitive to the panel temperature. On the other hand, from Fig. 5 (a) the Vt close-curve which was measured at point ② by applying the proposed ramp waveform in Fig. 4 (b), the Y-X discharge is produced at the lower side of the Y-X discharge side due to the ramp-type X bias applied to the X electrode, and as such relatively only a few weak Y-X discharges are not produced until the discharge point reaches the simultaneous discharge point. In this case, the weak reset discharge tends to be less sensitive to the panel temperature.

4. Address Discharge Robust Variable Panel Temperature as Result of Adopting Proposed Reset Waveform with Ramp-type X Bias

Fig. 6 (a) shows the IR emission during the reset-period measured at three different panel temperatures, i.e., –10, +25 and +80 °C where the conventional reset waveform of Fig. 4 (a) was applied. At a high panel temperature, the reset discharge is produced faster and the total amount

![Fig. 6. IR waveforms measured during reset discharges at various temperatures.](image)

![Fig. 7. IR waveforms measured during address discharge when applying conventional reset waveform of Fig. 4 (a).](image)
of the IR emission during the reset period is also increased, whereas the reset discharge characteristics at a low panel temperature shows the opposite to that at a high panel temperature. On the other hand, in the case where the proposed reset waveform with a ramp-type X bias was applied, the difference in the IR emission caused by the various panel temperatures tends to be minimized, as shown in Fig. 6 (b). As shown in Fig. 7, in the case of adopting the conventional of Fig. 4 (a), the different reset discharges at three different panel temperatures, i.e., –10, +25 and +80 °C cause the different address discharge.

As a result of adopting the proposed reset waveform of Fig. 4 (b), there seems little difference in the IR emission during the address discharge, which means that the proposed reset waveform contributes to alleviating the difference in the reset and subsequent address discharge caused by the changes in the panel temperature.

5. Conclusions

In the view of the discharge instability, the investigation on the relation between the discharge characteristics and the panel temperature is very important. This paper describes the experimental observation on the changes in the surface or plate gap discharge relative to the panel temperature ranging from –10 to 80 °C based on the Vt close-curve analysis. It is observed that the variable panel temperature can affect the X-Y surface discharge more than the A-Y plate gap discharge. The modified reset waveform, which can maximize the plate gap (A-Y) discharge by reducing the contribution of the surface (X-Y) discharge sensitive to the panel temperature, is proposed. The experimental observation confirms that the proposed reset waveform can produce a stable address discharge irrespective of the panel temperature variation.

References